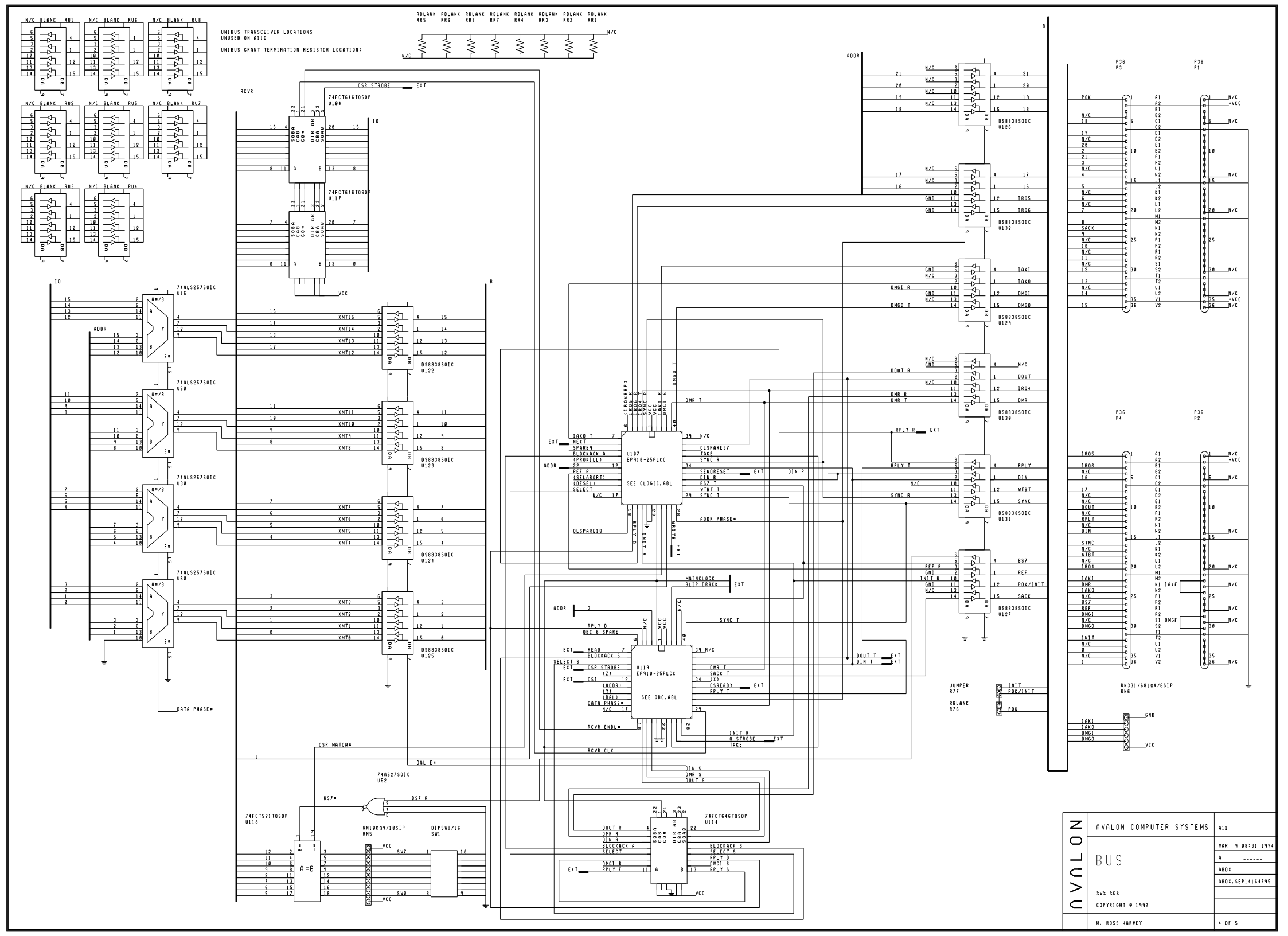
UNIBUS TRANSCEIVER LOCATIONS
UNUSED ON A110
UNIBUS GRANT TERMINATION RESISTOR LOCATION:
RBLANK RR5 RBLANK RR6 RBLANK RR8 RBLANK RR7 RBLANK RR4 RBLANK RR3 RBLANK RR2 RBLANK RR1
N/C
RCVR
CSR STROBE EXT
74FCT646TQSOP U104
74FCT646TQSOP U117
VCC
74ALS257SOIC U15
74ALS257SOIC U50
74ALS257SOIC U30
74ALS257SOIC U60
DS8838SOIC U122
DS8838SOIC U123
DS8838SOIC U124
DS8838SOIC U125
DATA PHASE*
ADDR
XMT15
XMT14
XMT13
XMT12
XMT11
XMT10
XMT9
XMT8
XMT7
XMT6
XMT5
XMT4
XMT3
XMT2
XMT1
XMT0
DS8838SOIC U126
DS8838SOIC U132
DS8838SOIC U129
DS8838SOIC U130
DS8838SOIC U131
DS8838SOIC U127
IAK1
IAK0
DMG1
DMG0
IRQ5
IRQ6
DOUT
IRQ4
DMR
BS7
REF
POK/INIT
SACK
SYNC
WTBT
RPLY
DIN
EP910-25PLCC U107
SEE OLOGIC.ABL
EP910-25PLCC U119
SEE OBC.ABL
74AS27SOIC U52
74FCT646TQSOP U114
74FCT521TQSOP U118
A=B
RN10K09/10SIP RN5
DIPSW8/16 SW1
JUMPER R77
RBLANK R76
INIT
POK/INIT
POK
P36 P3
P36 P1
P36 P4
P36 P2
RN331/68104/6SIP RN6
GND
VCC
AVALON COMPUTER SYSTEMS
A11
BUS
MAR 9 08:31 1994
A ------
ABDX
ABDX.SEP14164795
%W% %G%
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W. ROSS HARVEY
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